PTO/SB/08B (Substitute for form 1449/PTO)	Attorney Docket No.: *004-8515*
	Application No.: *10/620,747*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: *Mark S. Moir*
	Filing Date: *July 16, 2003*
	Group Art Unit: *2125*
	Examiner Name: *Not yet assigned*
Sheet 1 of 1	Date Submitted: *January 27, 2006*

IAP58
JAN 31 2006
PATENT & TRADEMARK OFFICE

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
I.T	1	AFEK, Yehuda, "Atomic Snapshots of Shared Memory," Journal of the ACM, vol. 40, no. 4, pp. 873-890, 1993.	
I.T	2	AGESEN, Ole at el., "An Efficient Meta-Lock for Implementing Ubiquitous Synchronization," ACM SIGPLAN Notices, vol. 34, no. 10, pp. 207-222, October 1999.	
I.T	3	ANDERSON, James H. et al., "Universal Constructions for Large Objects," *IEEE Transactions on Parallel and Distributed Systems*, vol. 10, no. 12, pp. 1317-1332, 1999.	
I.T	4	MELLOR-CRUMMEY, J., et al., "Algorithms for Scalable Synchronization on Shared-Memory Multiprocessors, *ACM Transactions on Computer Systems*, vol. 9, no. 1, pp. 21-65, February 1991.	
I.T	5	PRAKASH, Sundeep et al., "Non-Blocking Algorithms for Concurrent Data Structures," Technical Report 91-002, University of Florida, July 1, 1991 [URL: http://citeseer.ist.psu.edu/prakash91nonblocking.html].	
I.T	6	PRAKASH, Sundeep et al., "A Nonblocking Algorithm for Shared Queues Using Compare-and-Swap," *IEEE Transactions on Computers*, vol. 43, no. 5, pp. 548-559, May 1994.	
I.T	7	SHANN, Chien-Hua et al., "A Practical Nonblocking Queue Algorithm Using Compare-and Swap," *Proceedings of the Seventh International Conference on Parallel and Distributed Systems* p. 470, IEEE Computer Society, Washington, D.C., 2000.	
I.T	8	SHAVIT, N., et al., "Elimination Trees and the Construction of Pools and Stacks," *Theory of Computing Systems*, vol. 30, pp. 645-670, 1997.	
I.T	9	STONE, Janice M., "A simple and correct shared-queue algorithm using Compare-and-Swap," *Proceedings of the 1990 ACM/IEEE Conference on Supercomputing*, pp. 495-504, IEEE Computer Society, New York, NY, 1990.	
I.T	10	VALOIS, John D., "Lock-Free Linked Lists Using Compare-and-Swap," *Proceedings of the Fourteenth ACM Symposium on Principles of Distributed Computing, pp. 214-222*, ACM Press, New York, NY 1995.	

Examiner Signature		Date Considered	06/20/07

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Applicant's unique citation designation number (optional).

[2]Applicant is to place a check mark here if English language Translation is attached.

O.I.P.E IAP29
DEC 20 2005
PATENT & TRADEMARK OFFICE

Attorney Docket No.: 004-8515
Application No.: 10/620,747
Applicant(s): Mark S. Moir et al.
Filing Date: July 16, 2003
Group Art Unit: 2125
Date Submitted: December 15, 2005

LIST OF PENDING U. S. PATENT APPLICATIONS

The following pending U.S. patent applications are listed for consideration by the Examiner.

Examiner Initials	Cite No.	Document Identification
E.T	1	US Patent Application No. 09/547,288 (Attorney Docket No. 004-4663), filed April 11, 2000 and naming as inventor(s) Shavit et al.
E.T	2	US Patent Application No. 09/547,290 (Attorney Docket No. 004-4664), filed April 11, 2000 and naming as inventor(s) Shavit et al.
E.T	3	US Patent Application No. 09/710,218 (Attorney Docket No. 004-4896), filed November 10, 2000 and naming as inventor(s) Harris, Timothy
E.T	4	US Patent Application No. 09/837,671 (Attorney Docket No. 004-5723), filed April 18, 2001 and naming as inventor(s) Detlefs et al.
Examiner Signature		Date Considered: 07/18/07

Applicant(s) are providing a separate list of pending applications, pursuant to MPEP § 609III(D), to avoid publication of the application numbers on any patent that issues from the above-identified application. Applicant(s) request that the Examiner indicate consideration of the pending applications listed above in accordance with MPEP 609III(C)(2) and return a copy of this page with the next communication.

PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: *004-8515*
	Application No.: *10/620,747*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: *Mark S. Moir*
	Filing Date: *July 16, 2003*
	Group Art Unit: *2125*
	Examiner Name: *Unknown*
Sheet 1 of 2	Date Submitted: *December 15, 2005*

OIPE IAP20
DEC 20 2005
PATENT & TRADEMARK OFFICE

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
I.T		US-4,584,640	04-22-1986	MacGregor et al.	
		US-4,847,754	07-11-1989	Obermarck et al.	
		US-5,224,215	06-29-1993	Disbrow, John R.	
		US-5,319,778	06-07-1994	Catino, Robert J.	
		US-6,128,710	10-03-2000	Greenspan et al.	
		US-6,144,965	11-07-2000	Olivier, Richard J.	
		US-6,178,423	01-23-2001	Douceur et al.	
		US-6,360,219	03-19-2002	Bretl et al.	
		US-6,360,220	03-19-2002	Forin, Alessandro	
		US-6,366,932	04-02-2002	Christenson, David Alan	
		US-6,581,063	06-17-2003	Kirkman, Richard Karl	
I.I		US-6,651,146	11-18-2003	Srinivas et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
I.T		EP 0 366 585	05-02-1990	IBM		
		EP 0 466 339	01-15-1992	IBM		
		WO 86/00434	01-16-1986	Motorola		
		WO 01/53942	07-26-2001	Steele, Jr. et al.		
Examiner Signature				**Date Considered**	07/20/07	

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Applicant's unique citation designation number (optional).

[2]See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04.

[3]Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3)

[4]For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document.

[5]Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible.

[6]Applicant is to place a check mark here if English language Translation is attached.

PTO/SB/08A (Substitute for form 1449/PTO)	Attorney Docket No.: *004-8515*
	Application No.: *10/620,747*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: *Mark S. Moir*
(Continuation Sheet)	Filing Date: *July 16, 2003*
	Group Art Unit: *2125*
	Examiner Name: *Unknown*
Sheet 2 of 2	Date Submitted: *December 15, 2005*

U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Document Number Number-Kind Code[2] *(if known)*	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US-6,826,757	11-30-2004	Steele, Jr. et al.	
		US-2001/0047361	11-29-2001	Martin et al.	
		US-2003/0140085	07-24-2003	Moir et al.	
		US-2003/0174572	09-18-2003	Moir et al.	
		US-2003/0182462	09-25-2003	Moir et al.	
		US-2003/0182465	09-25-2003	Moir et al.	
		US-2004/0153687	08-05-2004	Moir et al.	

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No.[1]	Foreign Patent Document Country Code[3]-Number[4]-Kind Code[5] (If known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T[6]
		WO 01/53943	07-26-2001	Steele, Jr. et al.		
		WO 01/80015	10-25-2001	Martin et al.		
		WO 01/82057	11-01-2001	Moir et al.		
		WO 03/060705	07-24-2003	Moir et al.		
		WO 03/060715	07-24-2003	Moir et al.		
Examiner Signature				Date Considered	06/20/07	

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.:	004-8515
	Application No.:	10/620,747
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s):	Moir et al.
(Use several sheets if necessary)	Filing Date:	July 16, 2003
	Group Art Unit:	2125
	Date Submitted:	September 27, 2004

OIPE
SEP 29 2004
PATENT & TRADEMARK OFFICE

NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
I.T	1	Afek, Yehuda et al., "*Long-Lived Renaming Made Adaptive*", 18th Annual ACM Symposium on Principles of Distributed Computing, pages 91-104, 1999.
	2	Afek, Yehuda, "*Wait-Free Made Fast*", 27th Annual ACM Symposium on Theory of Computing, pages 538-547, 1995.
	3	Agesen, Ole et al., "*DCAS-Based Concurrent Deques*", 12th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 137-146, July 2000.
	4	Anderson, James H. et al., "*Using Local-Spin k-Exclusion Algorithms to Improve Wait-Free Object Implementations*", 12th Annual ACM Symposium on Principles of Distributed Computing, November 1995 (revised 1996, 1997).
	5	Arora, Nimar S. et al., "*Thread Scheduling for Multiprogrammed Multiprocessors*", 10th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 119-129, 1998.
	6	Attiya, Hagit et al., "*An Adaptive Collect Algorithm with Applications*", Dept. of Computing Science, The Technion, Israel, May 10, 2001.
	7	Barnes, Greg, "*A Method for Implementing Lock-Free Shared Data Structures*", 5th Annual ACM Symposium on Parallel Algorithms and Architectures, pages 261-270, 1993.
	8	Bayer, R. et al., "*Concurrency of Operations on B-Trees*", Acta Informatica, 1977.
	9	Detlefs, David L. et al., "*Even Better DCAS-Based Concurrent Deques*", 14th International Conference on Distributed Computing, pages 59-73, 2000.
	10	Detlefs, David L. et al., "*Lock-Free Reference Counting*", 20th Annual ACM Symposium on Principles of Distributed Computing, pages 190-199, 2001.
	11	Dice, David et al., "*Mostly Lock-Free Malloc*", ACM 2002.ACM SIGPLAN International Symposium on Memory Management, June 2002.
	12	Greenwald, Michael B., "*Non-Blocking Synchronization and System Design*", PhD Thesis, Stanford University Technical Report STAN-CS-TR-1624, Palo Alto, California, August 1999.
	13	Herlihy, Maurice, "*A Methodology for Implementing Highly Concurrent Data Objects*", ACM Transactions on Programming Languages and System, pages 745-770, November 1993.
	14	Herlihy, Maurice, "*Dynamic-Sized Lockfree Data Structures*", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.
	15	Herlihy, Maurice et al., "*Linearizability: A Correctness Condition for Concurrent Objects*", ACM Transactions on Programming Languages and Systems, pages 463-492, July 1990.
	16	Herlihy, Maurice et al., "*The Repeat Offender Problem: A Mechanism for Supporting Dynamic-Sized Lock-Free Data Structures*", Sun Microsystems Technical Report SMLI TR-2002-112, June 2002.

Examiner	Date Considered	06/20/07

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

Form 1449B

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.:	004-8515
	Application No.:	10/620,747
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s):	Moir et al.
(Use several sheets if necessary)	Filing Date:	July 16, 2003
	Group Art Unit:	2125
	Date Submitted:	September 27, 2004

NON PATENT LITERATURE DOCUMENTS

*Examiner Initial	Cite No.	(Including name of author in capital letters, title of article, title of item, date, pertinent pages, volume-issue number(s), publisher, city and/or country where published.)
	17	Herlihy, Maurice et al., "*Transactional Memory: Architectural Support for Lock-Free Data Structures*", 20th International Symposium in Computer Architecture, 1993.
	18	Herlihy, Maurice et al., "*Obstruction-Free Synchronization: Double-Ended Queues as an Example*", 23rd International Conference on Distributed Computing, May 2003.
	19	Israeli, Amos et al., "*Disjoint-Access-Parallel Implementations of Strong Shared Memory Primitives*", 13th Annual ACM Symposium on Principles of Distributed Computing, pages 151-160, 1994
	20	Lamport, Leslie, "*How to Make a Multiprocessor Computer that Correctly Executes Multiprocess Programs*", IEEE Transactions on Computers, September 1979.
	21	Luchangco, Victor et al., "*Nonblocking k-compare-single-swap*", 15th Annual ACM Symposium on Parallel Algorithms and Architectures, June 2003.
	22	Martin, Paul et al., "*DCAS-Based Concurrent Deques Supporting Bulk Allocation*", Sun Microsystems, Inc. Technical Report SMI TR-2002-111, October 2002.
	23	Michael, Maged M. et al., "*Non-Blocking Algorithms and Preemption-Safe Locking on Multiprogrammed Shared Memory Multiprocessors*", Journal of Parallel and Distributed Computing, March 1997.
	24	Michael, Maged M. et al., "*Simple, Fast and Practical Non-Blocking and Blocking Concurrent Queue Algorithms*", 15th Annual ACM Symposium on Principles of Distributed Computing, pages 267-276, 1996.
	25	Michael, Maged M., "*Safe Memory Reclamation for Dynamic Lock-Free Objects Using Atomic Reads and Writes*", 21st Annual ACM Symposium on Principles of Distributed Computing, pages 21-30, January 2002.
	26	Moir, Mark, "*Laziness Pays! Using Lazy Synchronization Mechanisms to Improve Non-Blocking Constructions*", 19th Annual ACM Symposium on Principles of Distributed Computing, 2000.
	27	Moir, Mark, "*Practical Implementations of Non-Blocking Synchronization Primitives*", 16th Annual ACM Symposium on Principles of Distributed Computing, 1997.
	28	Moir, Mark, "*Transparent Support for Wait-Free Transactions*", 11th International Workshop on Distributed Algorithms, 1997.
	29	Moir, Mark et al., "*Wait-Free Algorithms for Fast, Long-Lived Renaming*", Science of Computer Programming, August 1994.
	30	Saks, Michael et al., "*Optimal Time Randomized Consensus - Making Resilient Algorithms Fast in Practice*", 2nd ACM SIAM Symposium on Discrete Algorithms, pages 351-362, 1991.
	31	Shavit, Nir et al., "*Software Transactional Memory*", Distributed Computing, Special Issue (10), 1997.
	32	Trieber, R, "*Systems Programming: Coping with Parallelism*", IBM Technical Report RJ5118, April 23, 1986.
	33	Turek, John et al., "*Locking without Blocking: Making Lock Based Concurrent Data Structure Algorithms Nonblocking*", 11th ACM *SIGACT-SIGMOD-SIGART* Symposium on Principles of Database Systems, 1992.

Examiner	Date Considered	06/20/07

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

Form 1449B

U.S. Department of Commerce, Patent and Trademark Office	Attorney Docket No.: 004-8515
	Application No.: 10/620,747
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Applicant(s): Mark S. Moir et al.
(Use several sheets if necessary)	Filing Date: July 16, 2003
	Group Art Unit: 2125
	Date Submitted: June 17, 2004

U.S. Patent Documents

*Examiner Initial		Document Number	Date	Name
JT	1	5,428,761	Jun. 27, 1995	Herlihy et al.
JT	2	US2004/0015510 A1	Jan. 22, 2004	Moir et al.
JT	3	US2004/0015642 A1	Jan. 22, 2004	Moir et al.

Foreign Patent Documents

					Translation	
		Document	Date	Country	Yes	No

OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

Examiner JT Date Considered 06/20/07

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with your communication to applicant.

Form 1449

BEST AVAILABLE COPY

PTO/SB/08B (Substitute for form 1449/PTO)	Attorney Docket No.: *004-8515*
	Application No.: *10/620,747*
INFORMATION DISCLOSURE STATEMENT BY APPLICANT	First Named Inventor: *Mark S. Moir*
	Filing Date: *July 16, 2003*
	Group Art Unit: *2125*
	Examiner Name: *Unknown*
Sheet 1 of 1	Date Submitted: *December 28, 2005*

OIPE 3600
DEC 30 2005
PATENT & TRADEMARK OFFICE

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No.[1]	Include name of author (in CAPITAL LETTERS), title of article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T[2]
I.T	1	HERLIHY, M.P., "Wait-Free Synchronization," *ACM Transactions On Programming Languages and Systems*, 11(1):124–149, January 1991.	
I.T	2	MASSALIN, H., et al., "A Lock-Free Multiprocessor OS Kernel," Technical Report TR CUCS–005–9, Columbia University, New York, NY, 1991, 21 pages.	
I.T	3	MASSALIN, Henry, "Synthesis: An Efficient Implementation of Fundamental Operating System Services," Dissertation submitted in partial fulfillment of the requirements for the Degree of Doctor of Philosophy in the Graduate School of Arts and Sciences, Columbia University, New York, NY, online, 158 pages, 1992 [retrieved from the Internet on 2001-07-13: URL:ftp://ftp.cs.columbia.edu/reports/reports-1992/cucs-039-92.ps.gz].	
I.T	4	BERSHAD, B. N., "Practical Considerations For Non-Blocking Concurrent Objects," *Proceedings 13th IEEE International Conference on Distributed Computing Systems*, pp. 264–273. IEEE Computer Society Press, Washington, D.C., 1993.	
I.T	5	ATTIYA, Hagit, et al., "Are Wait-Free Algorithms Fast?" *Journal of the ACM*, 41(4):725–763, July 1994.	
I.T	6	LAMARCA, A., "A performance evaluation of lock-free synchronization protocols," *Proceedings of the 13th Annual ACM Symposium on Principles of Distributed Computing*, pp. 130–140, ACM Press, New York, NY, 1994.	
I.T	7	ATTIYA, H., et al., "Universal Operations: Unary versus Binary," *Proceedings of the 15th Annual ACM Symposium on Principles of Distributed Computing*, pp. 223-232, ACM Press, New York, NY, 1996.	
I.T	8	GREENWALD, M. B., et al., "The Synergy Between Non-Blocking Synchronization And Operating System Structure," *Proceedings of the 2nd Symposium on Operating Systems Design and Implementation*, pp. 123–136, Usenix Association, Berkeley, CA, 1996.	
I.T	9	AFEK, Y., et al., "Disentangling Multi-Object Operations," *Proceedings of the 16th Annual ACM Symposium on Principles of Distributed Computing*, pp. 111–120, August 1997. Santa Barbara, CA.	
I.T	10	ATTIYA, Hagit, et al., "Atomic Snapshots In O(n log n) Operations," *SIAM Journal on Computing*, 27(2):319–340, April 1998.	

Examiner Signature		Date Considered	07/24/07

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

[1]Applicant's unique citation designation number (optional).

[2]Applicant is to place a check mark here if English language Translation is attached.